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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#6/ae
S. Cotton

In re application of

Katsuyuki SUZUKI

Appln. No.: 09/357,990

Confirmation No.: not assigned

Filed: July 21, 1999

For: INITIALIZING/DIAGNOSING SYSTEM IN ON-CHIP MULTIPROCESSOR SYSTEM

RECEIVED 2-13-02

FEB 08 2002

Technology Center 2100

Group Art Unit: 2133

Examiner: P. CHUNG

AMENDMENT UNDER 37 C.F.R. § 1.111

Commissioner for Patents
Washington, D.C. 20231

Sir:

In response to the Office Action dated October 2, 2001, please amend the above-identified application as follows:

IN THE CLAIMS:

Claims 2 and 13 are canceled without prejudice or disclaimer.

Please enter the following amended claims:

1. (Amended) A semiconductor chip comprising:
a plurality of first elements each of which diagnoses itself; and
a second element which inputs diagnosis results from said first elements and determines whether or not there is a faulty first element in said first elements based on majority logic of said diagnosis results.

12. (Amended) A method which is performed in a semiconductor chip including a plurality of first elements, comprising:

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